

LOGIC ANALYZERS

HP 1660 Series Portable Logic Analyzers (cont'd.)

HP 1660A, 1661A, 1662A, and 1663A

Key Specifications and Characteristics

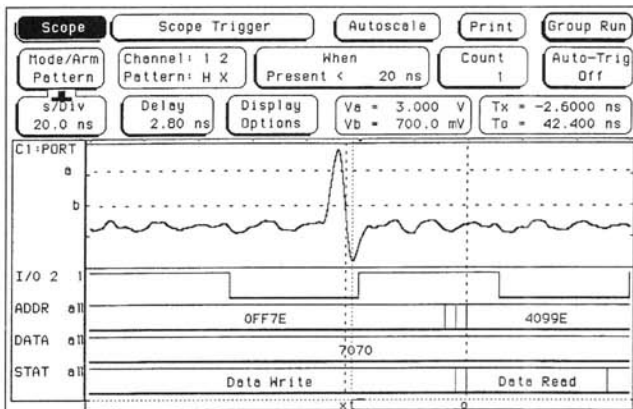
	HP 1660A, HP 1660AS	HP 1661A, HP 1661AS	HP 1662A, HP 1662AS	HP 1663A, HP 1663AS
State and timing channels	136	102	68	34
Timing analysis	Conventional: 250 MHz all channels, 500 MHz half channels Transitional: 125 MHz all channels, 250 MHz half channels Glitch: 125 MHz half channels			
State analysis	100 MHz in all modes			
State timing memory depth	4 K/channel, 8K in half-channel modes			
Setup/hold time	3.5/0 ns to 0/3.5 ns adjustable in 500 ps increments			
Min detectable glitch	3.5 ns			
Probe input R & C	100 K Ω and ~8 pF			
Trigger terms	Patterns: 10 Ranges: 2 Edge and glitch: 2 Timers: 2			
Trigger sequence terms	12 in state and 10 in timing			
Labels	126			
Symbols	1000			

Additional Information for HP 1660AS, HP 1661AS, HP 1662AS, and HP 1663AS

	HP 1660AS	HP 1661AS	HP 1662AS	HP 1663AS
Logic analysis capabilities	Identical to HP 1660A	Identical to HP 1661A	Identical to HP 1662A	Identical to HP 1663A
Scope channels	2			
Scope sample rate, bandwidth	1 Gsa/s, 250 MHz			
Scope vertical resolution	8 bits			
Scope memory depth	8k			

Label>	Time	ADDR	DATA	58040 Mnemonic
Base>	Relative	Hex	decimal (\$ = hex)	Dequeuing
1753	552 ns	F2008B8	CLR.B	(#0000065C,D0.L)
1754	504 ns	F2008BC	MOVE.L	D2,D0 suprgm
1755	544 ns	F2008C0	ADD.L	D0,D0
1756	X	504 ns	F2008C4	MOVE.L D0,D1
1757	96 ns	000066A	ASL.L	#3,D0
1758	504 ns	F2008C8	SUB.L	D1,D0
1759	496 ns	F2008CC	XXXX00xx	suprg data write
1760	0	552 ns	F2008D0	CLR.L (#00000666,D0.L)
1761	504 ns	F2008D4	ADDG.L	#1,D2
1762	96 ns	0000674	MOVEQ	#00000014,D0
1763	504 ns	F2008D8	CFP.L	D0,D2
			BLT.B	#F2008AC
			XXXX0000	suprg data write
			cnv MOVEQ	#00000000,D2

View your system's behavior with processor-specific mnemonics, using HP preprocessors and inverse assemblers.



Pinpoint design errors and show their effects by integrating time-correlated scope and timing waveforms.

Debug a Wide Range of Microprocessor-Based Systems Ranging from 8-Bit Controllers Up to 32-Bit Microprocessors

Select from over 160 solutions to connect to your design (see pages 338 to 340). The HP 1660 Series analyzers let you debug real-time software. State data can be displayed in processor-specific mnemonics to make debugging system problems easier. For popular microprocessor, new disassembler technology allows you to independently activate up to 10 unique filters, so software is easier to analyze. For example, you can focus on code flow by exclusively viewing calls, jumps, and returns.

View Analog Behavior of Digital Signals with Built-In Scope

The HP 1660AS, 1661AS, 1662AS, and 1663AS add two 1-GSa/s digitizing scope channels for viewing the analog behavior of your signals. Both channels capture non-repeating events simultaneously with 8k samples per channel. Characterize critical timing parameters with time interval measurements of better than 150 ps accuracy, or examine glitches in your system to determine if noise or loading is a problem.

The 166XAS models offer independent time and voltage markers, channel labeling, automatic pulse parameters, as well as edge, voltage level, and pattern duration triggering.

Enhance Troubleshooting with Full Analysis Features

As with all HP logic analyzers, the HP 1660 Series packs in numerous features designed to make debugging your system easier. You can view timing measurements in either waveform or listing formats. State measurements can be viewed in listing, compare listing, waveform, and X-Y chart displays. In state mode, use storage qualification to capture only the data you need to see, thereby using acquisition memory more efficiently.

Powerful Triggering

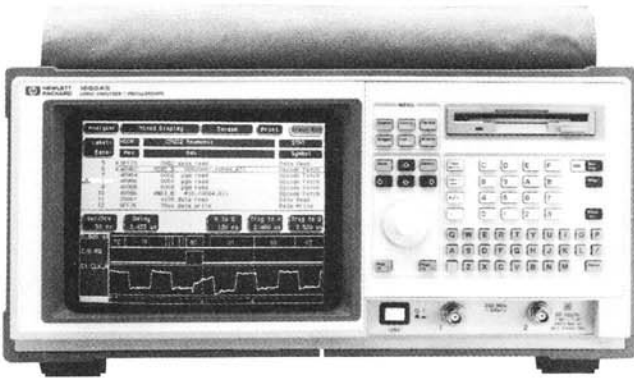
Triggering features help you capture elusive events. Start, center, end, and user-defined settings let you quickly specify the trigger position in memory. Use timers to trigger when a time-out or a data overrun condition occurs. Cross trigger the timing analyzer with the state analyzer to view the activity of control signals between states. Or trigger the scope using the logic analyzer's powerful triggering capabilities. Display the resulting time-correlated state, timing, and scope measurements on a single screen.

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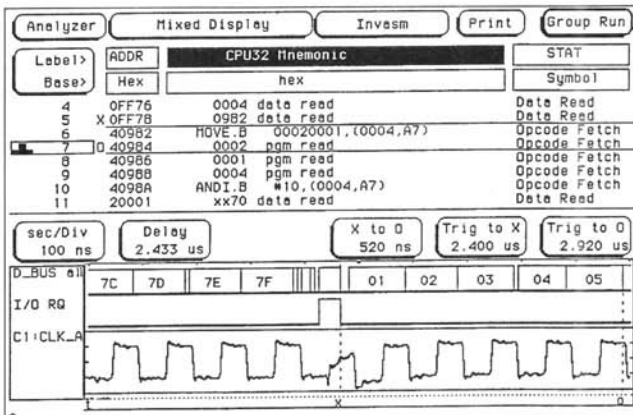
HP 1660 Series Portable Logic Analyzers

HP 1660AS, 1661AS, 1662AS, 1663AS, 1660A, 1661A, 1662A, 1663A, and 1651B

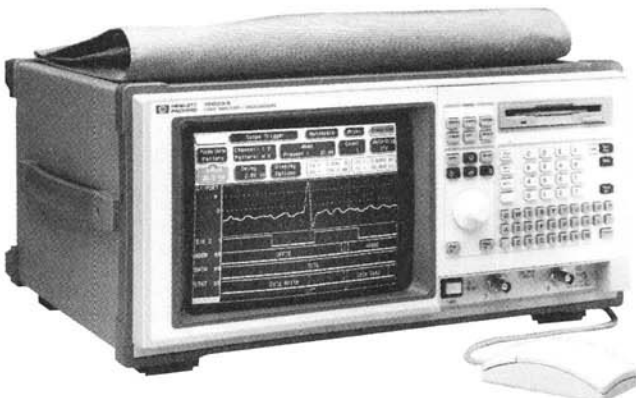
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HP 1660AS 136-channel state and timing analysis with 2-channel integrated oscilloscope.



Debug efficiently with time-correlated state, timing, and scope measurements on a single screen.



Move quickly through logically organized, graphical menus using front panel keys or the mouse, which ships standard.

Full-Featured Portable Logic Analyzers:

- Flexible channel count and measurement capabilities
- Over 160 microprocessor and bus interface solutions
- Easy-to-use graphical interface
- Mouse ships as standard equipment
- Built-in DOS format 3½-inch floppy disk
- Standard HP-IB and RS-232 ports
- Lightweight, flexible passive probing
- Compatible with HP 16550A logic analyzer module
- Compatible with HP 16532A oscilloscope module

HP 1660 Series Logic Analyzers

Confidently Deliver Products to Market Faster

You can feel confident about getting to the root-cause of digital design problems quickly and with certainty if you use an HP 1660 Series logic analyzer. As a result, your new product will probably get to market faster and work the way it was designed to work. HP 1660 Series logic analyzers have the right combination of performance, flexibility, and ease-of-use. The HP 1660 Series analyzers provide the needed power to solve your tough digital design problems. Yet, their graphical interface is so easy to use that you will want to use it on your not-so-tough problems too.

Application Flexibility

The HP 1660 Series covers the embedded designer's testing spectrum—from the isolation of elusive hardware problems to the debugging of real-time software.

Because your application determines the number of needed channels, Hewlett-Packard offers 34-, 68-, 102-, and 136-channel models in the HP 1660 Series.

Five modes of timing analysis let hardware designers adapt the analyzer to the speed and type of data they need to capture. And, with an optional 2-channel oscilloscope available with all models, you can open an analog window to digital events.

For software developers, we offer over 160 microprocessor and bus interface solutions. Powerful triggering lets you trace intricate software algorithms. For popular microprocessors, our new disassemblers enable you to focus on the specific code events you wish to view.

And for fast debugging during hardware/software integration, simultaneously capture state, timing, and oscilloscope data. The logic analyzer lets you view it all together in a mixed display so you can pinpoint design errors and show their effects.

Ease-of-Use

The intuitive user interface is easy to learn and re-learn later. A mouse comes standard so you can move quickly through logically organized, graphical menus. Front panel keys provide direct access to commonly used menus. HP 1660 Series logic analyzers can also be operated by an optional keyboard. A training kit ships standard and helps new users to feel comfortable in a short amount of time. The training kit includes its own target system so you can take real measurements.

Performance Comfort Zone

Don't worry about having enough performance for your new embedded design. The HP 1660 Series provides timing resolution up to 2 ns, oscilloscope sampling at 1 GSa/s, and state analysis that can keep up with system clocks as fast as 100 MHz.